CE-ATA Technical Errata

Errata ID	Protocol 023
Affected Spec Ver.	Protocol 1.1
Corrected Spec Ver.	

Submission info

Name	Company	Date
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Description of the specification technical flaw (add space as needed)

ATA mode defined in the MMCA reference specification has requirements that are not mandatory for a CE-ATA device. This errata removes mention of ATA mode from the CE-ATA specification. A CE-ATA device shall respond to RW_MULTIPLE_REGISTER (CMD60) and RW_MULTIPLER_BLOCK (CMD61) with no mode selection.

Description of the correction

The second paragraph of section 2.4.1 shall be removed as shown:

~~After completing the normal MMC reset and initialization procedures, the host should query EXT_CSD register byte 504 (S_CMD_SET) in MMC register space. If the ATA bit (bit 4) is set to one then the MMC device is an ATA device. If the device indicates that it is an ATA device, the host should set the ATA bit (bit 4) of the EXT_CSD register byte 191 (CMD_SET) to activate the ATA command set for use. The host selects the command set using the SWITCH (CMD6) command. Host implementations should be aware that CE-ATA devices compliant with the CE-ATA 1.0 specification may not support the ATA mode bits in the EXT_CSD register as the material was developed subsequent to the 1.0 specification.~~

Disposition log

3/28/2006	Erratum captured
5/29/2006	Erratum ratified

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